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# METAL INTERCONNECT LAYER OF SEMICONDUCTOR DEVICE AND METHOD FOR FORMING A METAL INTERCONNECT LAYER


## Abstract of the Disclosure

A metal interconnect layer of a semiconductor device, and a method for forming a metal interconnect layer of a semiconductor device are provided. The lower portion of a metal interconnect layer is wider than the upper portion of the metal interconnect layer. In another interconnect structure in accordance with the invention, the middle portion of the metal interconnect layer is wider than the upper and lower portions of the metal interconnect layer.


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